بِسۡمِ ٱللَّهِ ٱلرَّحۡمَٰنِ ٱلرَّحِيمِ

کیا آپ نے آج درود شریف پڑھا

نہیں تو ابھی پڑھ لیجئے

صلی اللہ علیہ وسلم

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المٓصٓ	حٰمٓ	یٰسٓ
کٓهٰیٰعٓصٓ	قٓ	اٰمٓینَ

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اس کے دیکھنے والوں کی سب مشکلیں آسان ہو جائیں گی صبح اس کو دیکھ کر جو کام شروع کیا جائیگا' پورا ہوگا' اور غیبی طریقوں سے رزق آنے لگے گا!

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امید کرتا ہوں سب خیریت سے ہوں گئے۔ آنے والے تمام نیو طالب علم کو میں ورچوئل یونیورسٹی آف پاکستان میں خوش آمدید کرتا ہوں۔

اگر کسی سٹوڈنٹ کو کسی بھی قسم کی ہیلپ چاہے ہو تو ہم سے رابطہ کر سکتا ہے جیسا کہ: پاسٹ پیپرز، کورس سلیکشن، پروجیکٹ سلیکشن، ڈیٹ شیٹ، فیس پے اور چینج سٹڈی پروگرام وغیرہ۔

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انشاءاللہ آپ ہماری سروس سے مطمئن ہوں گے۔

Email:
waqasahmeduog@gmail.com

Whatsapp: 03066295623

مزید اگر کسی رہنمائی کی ضرورت ہو تو وہ رابطہ کر سکتا ہے۔

CS501 - Advance Computer Architecture (Quiz#03)

Question # 5 of 10 (Start time: 10:52:22 AM, 11 August 2022)

Which is the last instruction of the ISR that is to be executed when the ISR terminates?

Select the correct option

- (●) IRET
- ( ) IRQ
- ( ) NMI
- ( ) INT

CS501 - Advance Computer Architecture (Quiz#03)

Question # 6 of 10 (Start time: 10:52:54 AM, 11 August 2022)

_____ is/are example(s) of synchronous communication.

Select the correct option

- Memory to Memory
- All of the given
- Register to Register
- Register to Memory

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Question # 4 of 10 (Start time: 10:51:33 AM, 11 August 2022)

_____ signal has input direction with respect to printer

Select the correct option

- PE#
- BUSY

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- STROBE#
- ACKNLG#

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Question # 6 of 10 (Start time: 10:52:54 AM, 11 August 2022)

_____ is/are example(s) of synchronous communication.

Select the correct option

- Memory to Memory
- All of the given
- Register to Register
- Register to Memory

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Question # 7 of 10 (Start time: 10:53:39 AM, 11 August 2022)

Which of the following pins of processor is designated for maskable interrupts?

Select the correct option

- MI
- NMI
- INTR
- RINT

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Question # 8 of 10 (Start time: 10:54:14 AM, 11 August 2022)

Identify the following type of serial communication error condition in which no character is available at the begin

Select the correct option

- ○ Framing error
- ○ Overrun error
- ○ Parity error
- ◉ Under-run error

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Question # 9 of 10 (Start time: 10:54:43 AM, 11 August 2022)

Which one of the following is NOT a technique used when the CPU wants to exchange data with a peripheral devi

Select the correct option

○	Direct Memory Access (DMA)
○	Programmed I/O
○	Interrupt driven I/O
◉	Virtual Memory

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Question # 5 of 10 (Start time: 06:18:52 PM, 17 August 2022)

In DECIDE frame work choosing the evaluation paradigm and techniques comes just after _____.

Select the correct option

- (●) Exploring the questions
- ( ) Deciding about ethical issues
- ( ) Resolving the issues of users
- ( ) Identifying the practical issues

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Question # 6 of 10 (Start time: 06:20:11 PM, 17 August 2022)

Evaluations done during design to check that product continues to meet users' needs are known as _____ evalu

Select the correct option

- (●) Formative
- ( ) Summative
- ( ) Opposite
- ( ) Relative

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Question # 7 of 10 (Start time: 06:20:37 PM, 17 August 2022)

Global Navigation system is also called_____.

Select the correct option

- ○ Web Navigation
- ◉ Persistent Navigation
- ○ Site ID
- ○ Address Navigation

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Question # 8 of 10 (Start time: 06:21:18 PM, 17 August 2022)

_____ applications are great platforms for creating an environment rich in visual feedback for the user.

Select the correct option

- Transient
- Sovereign
- Daemonic
- Auxiliary

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Question # 10 of 10 (Start time: 06:22:55 PM, 17 August 2022)

Panes, frames and other containers on screen is a kind of _____.

Select the correct option

- Input method
- Form factor
- View
- Functional and data elements

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Question # 1 of 10 (Start time: 10:40:23 AM, 11 August 2022)

_____ is a technique in which some of the CPU's address lines forming an input to the address decoder ar

Select the correct option

- (●) Partial decoding
- ( ) Instruction pre-fetching
- ( ) Microprogramming
- ( ) Pipelining

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Question # 2 of 10 (Start time: 10:40:53 AM, 11 August 2022)

Which is the last instruction of the ISR that is to be executed when the ISR terminates?

Select the correct option

- ○ INT
- ○ IRQ
- ○ NMI
- ◉ IRET

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Question # 3 of 10 (Start time: 10:41:13 AM, 11 August 2022)

Identify the type of serial communication error condition in which "0" is received instead of a stop bit (which is alw

Select the correct option

○	Parity error
◉	Framing error
○	Overrun error
○	Under-run error

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Question # 4 of 10 (Start time: 10:42:24 AM, 11 August 2022)

In a printer with DB-25 interface, _____ signal is is better for edge triggered systems.

Select the correct option

- (●) ACKNLG#
- ( ) PE#
- ( ) STROB#
- ( ) BUSY#

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Question # 5 of 10 (Start time: 10:43:58 AM, 11 August 2022)

A computer interface is an _____ circuit that matches the requirements of the two subsystems betwee

Select the correct option

- ○ Obituary
- ○ Digital
- ○ Primary
- ◉ Electronic

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Question # 5 of 10 (Start time: 10:43:58 AM, 11 August 2022)

A computer interface is an _____ circuit that matches the requirements of the two subsystems betwee

Select the correct option

○	Obituary
○	Digital
○	Primary
◉	Electronic

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Question # 6 of 10 (Start time: 10:44:37 AM, 11 August 2022)

How Interrupt driven I/O is better than polling because?

Select the correct option

○	Interrupt driver I/O is enhanced version of polling.
○	Interrupt driver I/O is easy to design
○	Interrupt driven I/O is easy to program.
◉	Interrupt driver I/O does not waste time on checking which device is available.

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Question # 7 of 10 (Start time: 10:44:58 AM, 11 August 2022)

In which technique does the hardware directly access host memory for reading or writing independent of CPU?

Select the correct option

- ○ Polling
- ○ Programmed I/O
- ◉ Direct Memory Access (DMA)
- ○ Interrupt driven I/O

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Question # 8 of 10 (Start time: 10:45:53 AM, 11 August 2022)

Which one of the following methods for resolving the priority makes use of individual bits of a priority encoder?

Select the correct option

- ○ Asynchronous Priority
- ○ Semi-synchronous Priority
- ◉ Parallel Priority
- ○ Daisy-Chaining Priority

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Question # 10 of 10 (Start time: 10:47:26 AM, 11 August 2022)

Connection to a CPU that provides a data path between the CPU and external devices, such as a keyboard, displ

Select the correct option

- ○ Processor
- ○ Memory mapping
- ○ Buffer
- ◉ I/O port

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Question # 1 of 10 (Start time: 10:49:23 AM, 11 August 2022)

An interface that can be used to connect the microcomputer bus to _____is called an I/O Port.

Select the correct option

- (●) Peripheral devices
- ( ) Multiplexers
- ( ) Memory
- ( ) Flip Flops

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Question # 2 of 10 (Start time: 10:49:55 AM, 11 August 2022)

Which is the last instruction of the ISR that is to be executed when the ISR terminates?

Select the correct option

- ○ IRQ
- ○ INT
- ○ NMI
- ◉ IRET

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Question # 3 of 10 (Start time: 10:50:21 AM, 11 August 2022)

Tri-state buffers are used for removing _____.

Select the correct option

- Instruction contention
- bus collision
- Instruction collision
- bus contention

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